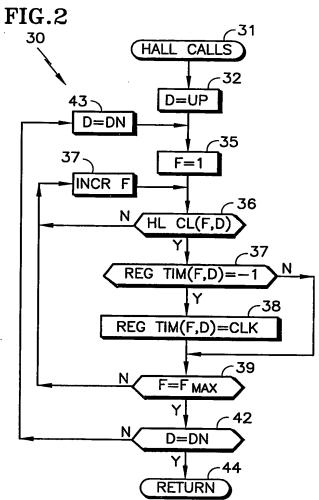
1/5 FIG.1 12 \ 13 \ **SENSORS DATA** 11 <sub>-</sub>15 1/0 24 000 DATA BUS 000 000 20 28-27~ 26-000 000 1/0 18 **CPU** RAM **ROM** 000 20 000 000 ADDRESS BUS 000 25<sup>-</sup> SIGNAL PROCESSOR UP DOWN UP DOWN ŲΡ **DOWN** 





LIM (ALT)

LIM (ALT)

130

LIM (ALT)

131

CLS/MIN>P

Y

132

LIM=K1\*CLS/MIN

136

CLS/MIN<Q

Y

137

LIM=K2\*CLS/MIN

RETURN



